



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	10/678,513
Filing Date	October 3, 2003
Inventor	
Assignee	
Group Art Unit	
Examiner	
Attorney's Docket No	MI22-2391
Title: Semiconductor Processing Methods	

## EXAMINER INTERVIEW SUMMARY OF October-22, 2007

To:

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

From:

D. Brent Kenady Wells St. John P.S.

601 West First Avenue, Suite 1300

Spokane, WA 99201-3828

(Tel. 509-624-4276; Fax 509-838-3424)

Applicant's representative held an interview with Examiner Kebede on October 22, 2007. Applicant's representative would like to thank Examiner Kebede for his time and attention to this matter.

Applicant's representative notified Examiner Kebede that the amended title presented in the last response was not provided in the Notice of Allowance. The Examiner stated he did not have a problem with the amended title and that the correct title should be handled appropriately at the publication stage. The correct title is: "Semiconductor Processing Methods."

Applicant's representative would like to again thank Examiner Kebede for considering this matter.

Respectfully submitted,

Dated: 10-24-07

By:

D. Brent Kenady Reg. No. 40,045

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